







## **Typical Features**

- ◆ Wide input voltage, non-Isolated & regulated single output
- ◆ High Efficiency up to 96%
- ◆ Small compact SIP packing
- ◆ Short circuit, Over-heat Protection
- ◆ Low ripple and noise
- ◆ Operating Temperature: -40°C~+85°C
- ◆ Plastic Case, meet UL94 V-0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25°C

Typical Product List								
Part No.	Input Voltage Range (VDC)	Voltage	tput /Current /Io)	Quiescent Current (mA)	Capacitive Load	Ripple & Noise ( mVp-p)	Efficien	cy (%)
		Voltage (VDC)	Current (mA)	Тур.	uF	Тур.	Vin (min)	Vin (max)
K783V3-500	24 (4.75 – 36)	3.3	500	5	1000	30	80	90
W7005 500	24 (6.5 – 36)	5	500	5	1000	30	84	93
K7805-500	12 (6.5 – 27)	-5	-400	5	470	30	70	80
*K7809-500	24 (12 – 36)	9	500	5	1000	30	91	94
1/7010 500	24 (15 – 36)	12	500	5	1000	30	92	95
K7812-500	12 (7 – 20)	-12	-200	5	470	30	75	83
K7815-500	24 (19 – 36)	15	500	5	1000	30	93	96

Note: "\*" is model under developing.

Input Specifications					
Item	Operation Conditions	Min.	Тур.	Max.	Unit
No load power consumption	Input voltage range		0.12	0.256	W
Reverse the input			Not al	lowed	
Input filter Capacitor filter					

Output Specifications						
Item	Working Conditions		Min.	Тур.	Max.	Unit
Output Voltage Accuracy	Full load			±2	±3	%
Ripple & Noise*	Nominal input, full	Positive output		25	35	m\/n n
	load, 20MHZ bandwidth	Nogotivo output		25	40	mVp-p
Load Regulation	10% ~ 100% load			±0.4	±0.6	%



# DC/DC Converter K78XX-500 Series



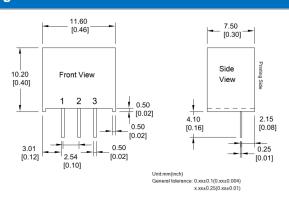


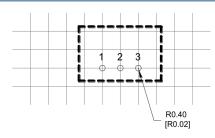
Line Regulation	Input Voltage Change		±0.2	±0.4	%
Temperature Drift Coefficient	100% Load			±0.03	%/°C
Over Heat Protection	IC build-in		150		°C
Output Short Circuit Protection		Continuous, Self-recovery			

NOTE: Ripple & Noise tested by twisted-pair method;

<b>General Specifications</b>		
Switching Frequency	Typical	350KHz (Typ.)
Operating Temperature	Refer to Temperature Derating Curve	-40°C ~+85°C
Storage Temperature		-55℃ ~+125℃
Shell temperature rise during work		100°C (MAX.)
Relative Humidity	No condensing	5%~95%
Case Material		Black flame-retardant heat-resistant Plastic (UL94 V-0)
Product Weight		2.0g (Typ.)
Pin Withstand Soldering Temp	Distance to case 1.5mm, 10S	300℃
MTBF	MIL-HDBK-217F@25℃	20X10 <sup>5</sup> Hrs
Dealeana	Tube(525*18*10mm)	43PCS
Package	Box(542*110*155mm)	3440pcs(total 80 Tubes)

## **Packing Dimension**





Printed board vertical view

Lattic spacing:2.54mm(0.1inch)

Packing Code	LxWxH				
K78XX-500	11.60× 7.50 × 10.20mm		0.457 x 0.295x 0.402inch		
Pin-Function					
Pin-Out	1	2	3		
Positive Output	+Vin	GND	+Vo		
Negative Output	+Vin	- Vo	GND		









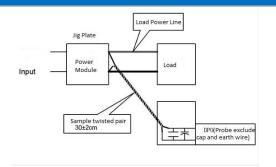
Note: if the definition of pin is not in accordance with the manual, please refer to the label on actual item.

### Ripple& Noise Test: (Twisted Pair Method, 20MHz bandwidth)

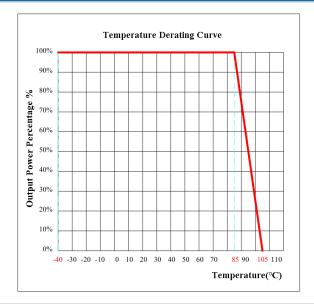
Test Method:

a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



#### **Temperature Curve**



**Design and Application Circuit Reference** 



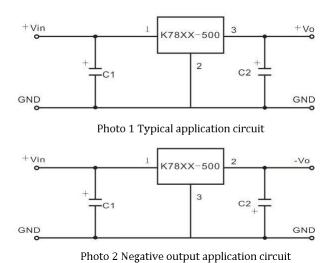


#### 1. Output Load Request

- a. To ensure this module operate efficiently and reliably, the minimum load could not be less than 10% of the nominal load. If the actual power is too small, please parallel a resistor at output terminal, the resistance equal to 10% of nominal load.
  - b. The maximum capacitive load is tested under nominal input voltage with full load, and cannot exceed the maximum capacitive load of output side when using, or it will be difficult to start up and damage the product.

#### 2. Recommended Circuit

In order to ensure that the ripple& noise of input and output be decreased, a capacitor filter net could be connected to input output side, application circuit see photo 1; negative output application circuit see below Table 2, But a proper filter capacitor should be chosen, start up problem maybe caused by too big capacitance; to ensure each circuit work safely and reliably, recommended capacitive load value see below table 1.(The capacitance of C1,C2 refer to external capacitor table, and could be increased properly, also could use low ESR of tantalum capacitor and electrolytic capacitor)



Model	C1 Ceramic capacitor	C2 Ceramic capacitor
K783V3-500	10uF/50V	22uF/6.3V
K7805-500	10uF/50V	22uF/10V
K7812-500	10uF/50V	10uF/25V
K7815-500	10uF/50V	10uF/25V

#### Note:

- 1. This product cannot be used in parallel, and do not support hot-plugging;
- 2.All index testing methods in this datasheet are based on our Company's corporate standards
- 3. The product specification may be changed at any time without prior notice.